# digit_testbench



parameters.conf

- v_measure_window
- v_comm_protocol
- v_bytes_per_frame
- v_UART_bps
- v_UART_stop_bits
- v_SPI_CPOL
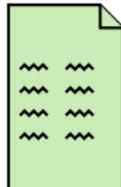- v_SPI_CPHA

RESET

READ GENERICS

- measure_window
- comm_protocol
- bytes_per_frame
- UART_bps
- UART_stop_bits
- SPI_CPOL
- SPI_CPHA

reset

FMEAS

COUNT EDGES    MEASURE WINDOW

WRITE FREQUENCIES    UART TRANSMITTER    SPI SLAVE
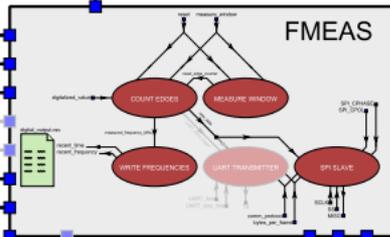
analog_output.csv

- recent_time
- recent_value

A/D

digitalized_value

analog_value

GENERATE ANALOG
SIGNAL

TX

UART RECEIVER

SCLK  SS  MISO

SPI MASTER
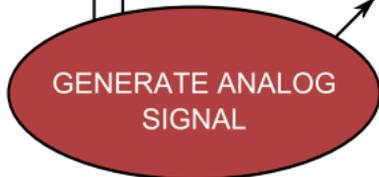
port

signal / variable

file

process

instance