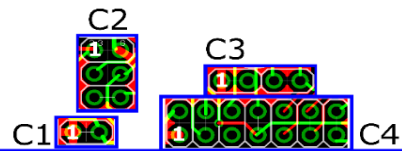


# LDCU PCB pin configurations

LDCU PCB layout top



C1

1	2
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- 1 GND
- 2 5V IN

C2

1	2
3	4
5	6

- 1 3.3V to Yitran modem
- 2 GND
- 3 Yitran modem TX (data from Yitran modem)
- 4 Yitran reset input
- 5 NC
- 6 Yitran RX (data to Yitran modem)

C3

1	2	3	4
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- 1 GND
- 2 3.3V IN
- 3 I2C SDA
- 4 I2C SCL

C4

2	4	6	8	10	12	14
1	3	5	7	9	11	13

- 1 GND
- 2 5V IN
- 3 Yitran reset IN
- 4 Yitran RX (data to Yitran modem)
- 5 NC
- 6 Yitran TX (data from Yitran modem)
- 7 1.8V IN
- 8 3.3V IN
- 9 FTDI reset IN
- 10 FTDI reset OUT
- 11 LDCU TX (data from LDCU)
- 12 LDCU RX (data to LDCU)
- 13 I2C SDA
- 14 I2C SCL