

# **Advanced experimental approaches and applications**

## **Lecture by Chung, Jae-Hyun**

Chung, Jae-Hyun, Mechanical Engineering, University of Washington

Liu, Wing Kam, Mechanical Engineering, Northwestern University

Liu, Yaling, Mechanical and Aerospace Engineering, University of Texas at Arlington



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## **Major nanolithography**

	Process	Wave length	Resolution	Throughput
UV	Parallel	~ 400 nm	2 μm	Excellent
Deep UV	Parallel	248 nm	0.5 μm	Excellent
Extreme UV ✓	Parallel	13.4 nm	50 nm (30nm)	Excellent
E-beam lithography	Serial	< 1 nm	10 nm	Fair
SPM	STM	Serial	N/A	Fair
	Dip-pen	Serial	N/A	Fair
Soft litho	μ-contact printing	Parallel	N/A	Very good
	NIL ✓	Parallel	N/A	Very good
X-ray	Parallel	< 1 nm	100 (20 nm)	Very good
?	High resolution, high throughput lithography is required.			

Low cost, high throughput nanomanufacturing is essential for biochips.



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## Shadow Effect

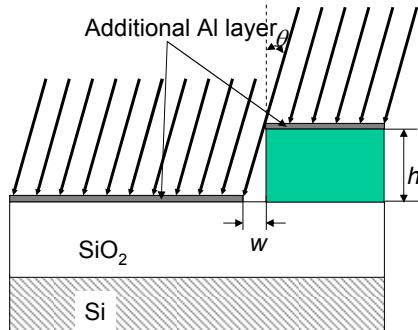


Electron beam evaporator  
Washington technology center

▪ “line-of-sight” deposition in high-vacuum evaporation.

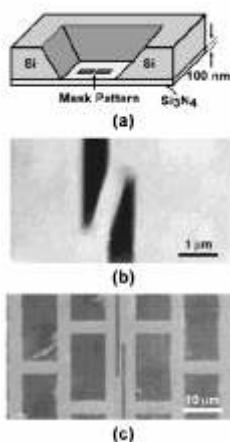
▪ When the deposition angle is  $\alpha$  and the thickness the pre-patterned layer is  $h$ , the nanogap width is

$$w = h \tan \alpha$$



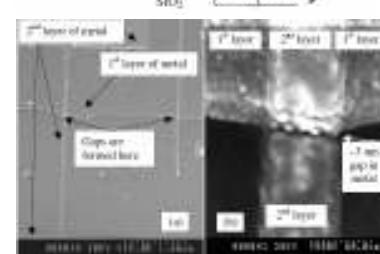
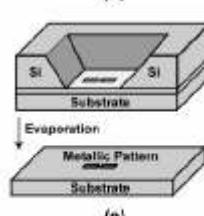
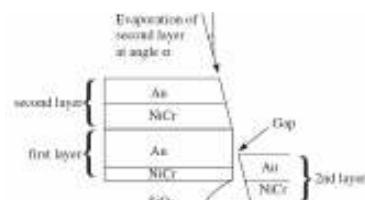
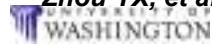
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## Shadow effect for nanopatterning



Patterning using nanoscale stencil

Zhou YX, et al., *Nano Letters*, 2003



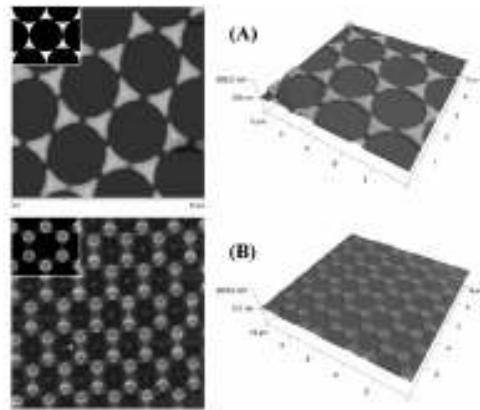
Nanogap creation

Sun LF, et al, *Nanotechn., 2005*

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## Shadow effect for nanopatterning (cont'd)



Simulation and experiment

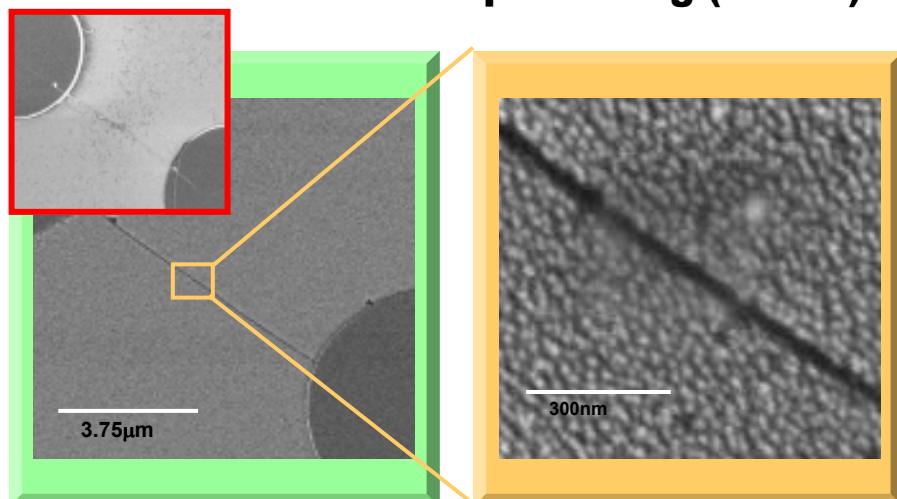
Shadow nanosphere lithography

Kosiorek A, et al., *NANO LETTERS*, 2004.

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## Shadow effect for nanopatterning (cont'd)



Nanotube assembly-> pattern reversal-> nanogap

Chung J et al., *Nano Letters*, 2003

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## Lithography using the Shadow Effect?

### Pros

- It is demonstrated that a few nanometer scale is working (High resolution).
- Evaporation is performed for a whole wafer (High throughput).
- Simulation matches experiment quite well.
- Depending on angle and perpatterned shapes, various patterns can be conceivable.

### Cons

- Theoretical model is not established.
- Wafer scale fabrication is yet to be shown.
- Geometrical variation across a wafer was not considered.
- The prepatterned layers or particles have defects.

### PROPOSE Shadow edge lithography

- Prepatterned Al layer will be used to create nanoscale patterns.

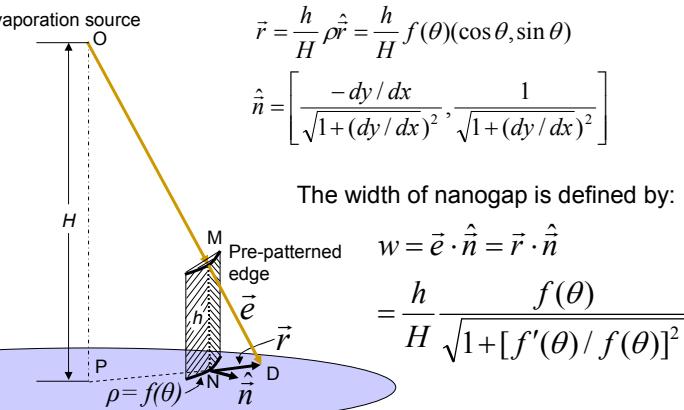


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## Shadow width; Point Source

A shadow edge on the deposition plane with an arbitrary shape can be expressed by:

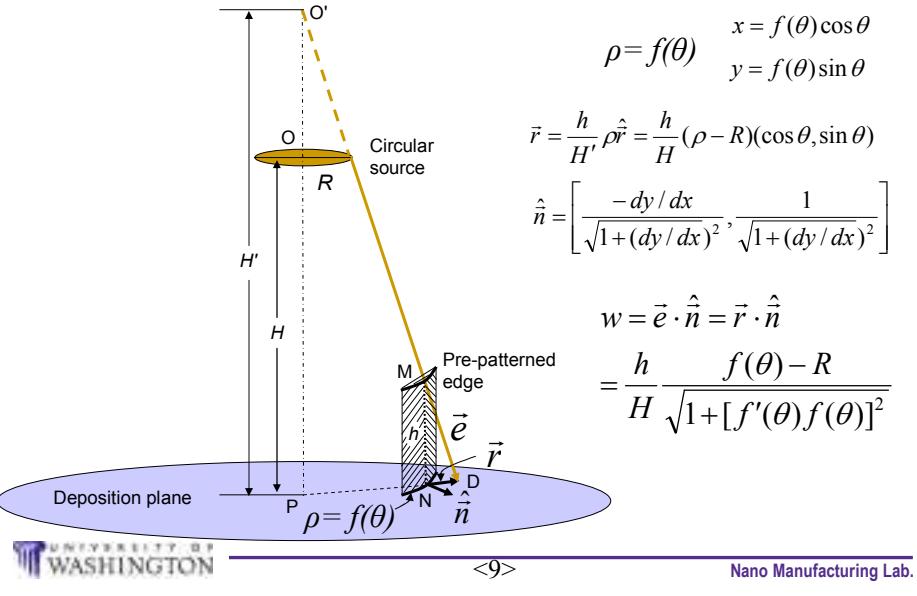
$$\rho = f(\theta) \quad \text{In the corresponding Cartesian coordinates:} \quad x = f(\theta) \cos \theta \\ y = f(\theta) \sin \theta$$



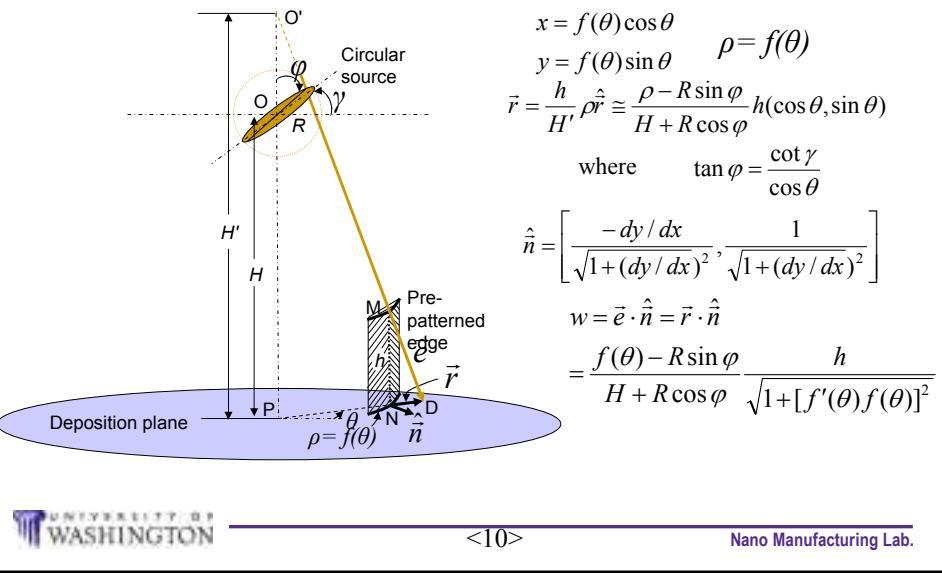
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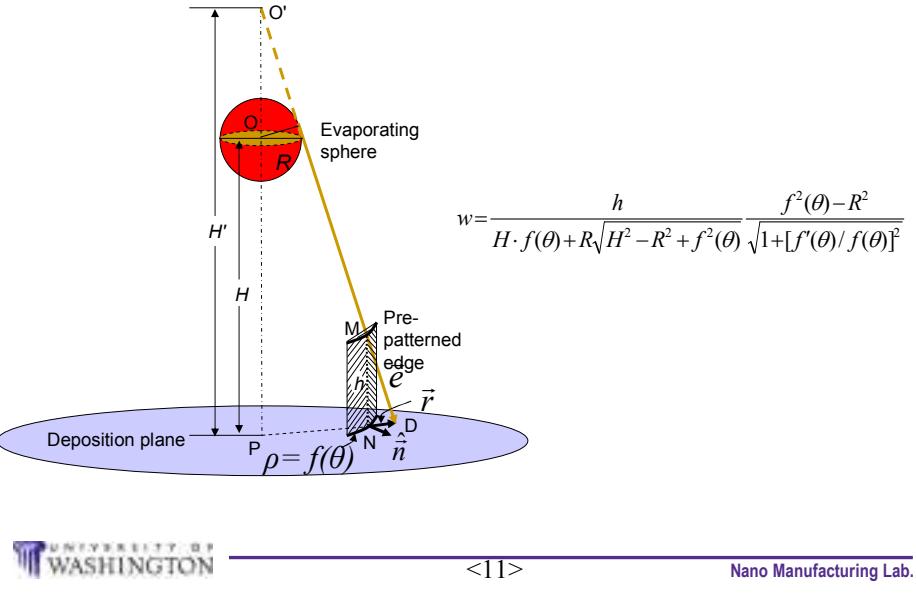
## Shadow width; Parallel, Circular Source



## Shadow width, Non-Parallel, Circular Source



## Shadow width; Spherical Source



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## Summary of Shadow width

The shadow width of the pre-patterned edges with different shapes on a deposition plane

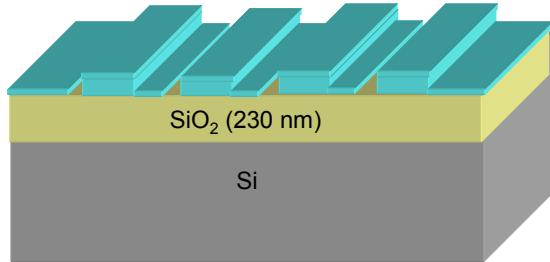
Evaporation sources (with a radius $R$ and a deposition height $H$ )	Shapes of shadow edge (with a uniform height $h$ )		
	Arbitrary shape: $\rho = f(\theta)$	Straight line: $\rho = \frac{\rho_0}{\cos \theta}$	Center circle: $\rho = \rho_0$
Spherical source	$w = \frac{h}{H \cdot f(\theta) + R \sqrt{H^2 - R^2 + f^2(\theta)}} \frac{f^2(\theta) - R^2}{\sqrt{1 + [f'(\theta)/f(\theta)]^2}} h$	$w = \frac{\rho_0^2 - R^2 \cos^2 \theta}{H \cdot \rho_0 + R \sqrt{H^2 - R^2} \cos^2 \theta + \rho_0^2} h$	$w = \frac{\rho_0^2 - R^2}{H \cdot \rho_0 + R \sqrt{H^2 - R^2 + \rho_0^2}} h$
Circular source (tilted case)	$w = \frac{h}{H + R \cos \phi} \frac{f(\theta) - R \sin \phi}{\sqrt{1 + [f'(\theta)/f(\theta)]^2}}$ (where $\tan \phi = \cot \gamma / \cos \phi$ )	$w = \frac{\rho_0 - R \sin \phi \cos \theta}{H + R \cos \phi} h$	$w = \frac{\rho_0 - R \sin \phi}{H + R \cos \phi} h$
Circular source (parallel case)	$w = \frac{h}{H} \frac{f(\theta) - R}{\sqrt{1 + [f'(\theta)/f(\theta)]^2}}$	$w = \frac{\rho_0 - R \cos \theta}{H} h$	$w = \frac{\rho_0 - R}{H} h$
Point source ( $R = 0$ )	$w = \frac{h}{H} \frac{f(\theta)}{\sqrt{1 + [f'(\theta)/f(\theta)]^2}}$	$w = \frac{\rho_0}{H} h$	$w = \frac{\rho_0}{H} h$



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## Nanogap fabrication steps



Si wafer- Oxidation- Al conformal evaporation-Lithography –Al non conformal evaporation- **Shadow gap**

- Why Al?** Good for evaporation and etching
- Good for masking layer to reactive ion etching
- Easy height variation
- Easy selective etching- pattern reversal is enabled



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## Experimental—2<sup>nd</sup> Al deposition

1<sup>st</sup> Al deposition:

Conformal deposition: uniform Al height.

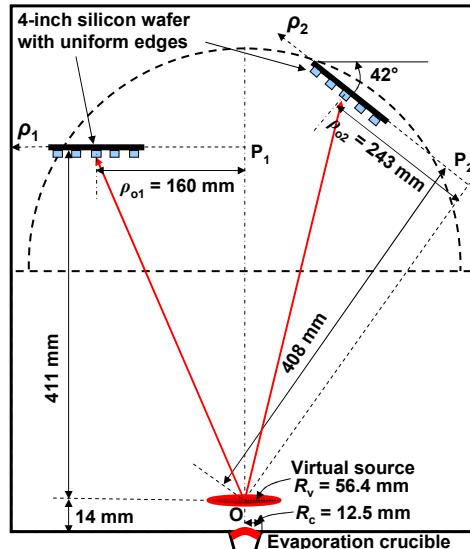
2<sup>nd</sup> Al deposition:

Nonconformal deposition at fixed angles.

85, 120, and -t, -p

85, 120, 180- thickness (nm)

t: tilted, p: parallel



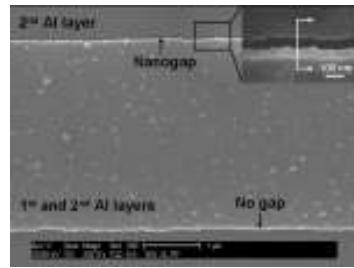
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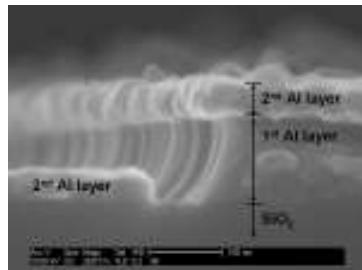
## Shadow gap shape



Various Al patterns for shadow edges

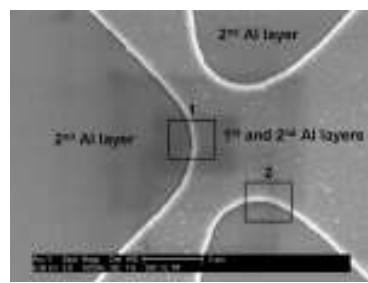


Nanogap on a straight Al stripe

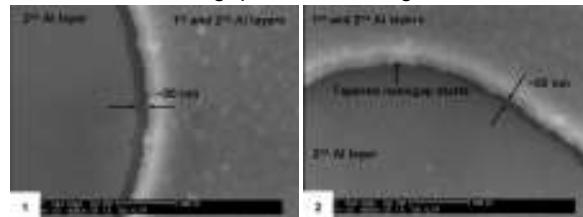


Cross-section of a nanogap

## Shadow gap shape (cont'd)

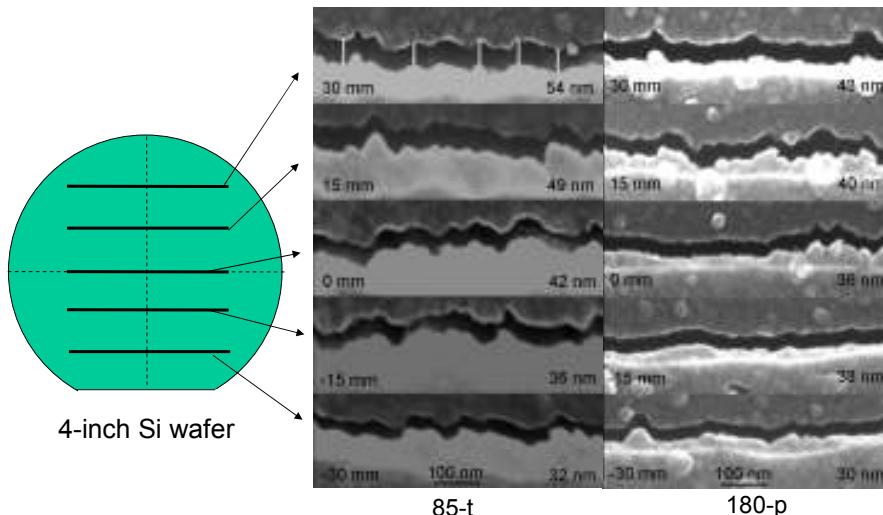


Shadow gap for curved edges



Crescent nanogaps; gradual variation of nanogaps due to the Al edge patterns

## Experimental Results —without Compensation

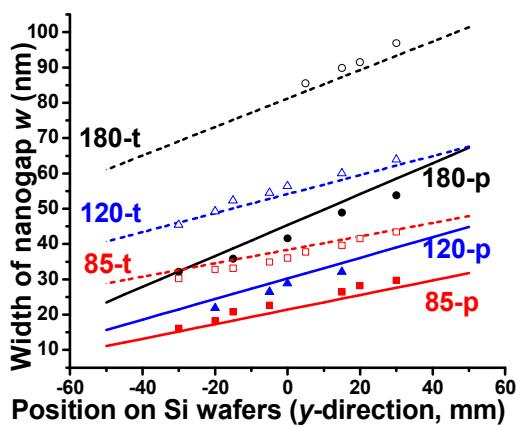


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## Shadow gap variation on 4-in wafer



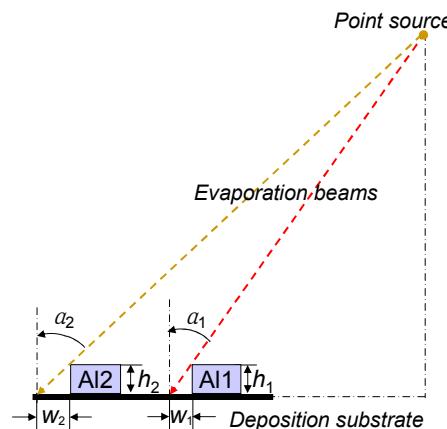
Nanogap width variation along the  $y$ -direction of the 4-inch Si wafers  
Approximately  $\pm 10$  nm variation in 60mm.

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## Compensation for uniform gap width



- When the deposition angle is  $\alpha$  and the thickness the pre-patterned Al layer is  $h$ , the nanogap width is

$$w = h \tan \alpha$$

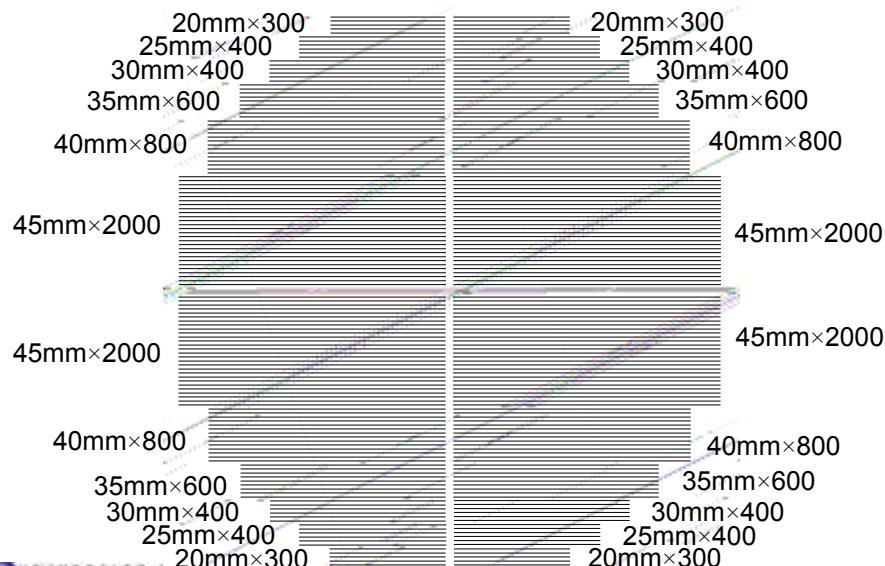
- Gap width is varying due to the angle ( $\alpha$ ) change across wafer.

- The angle variation can be compensated by the height variation of the prepatterned Al.



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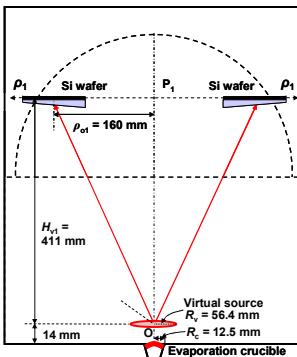
## Mask pattern for compensation experiment



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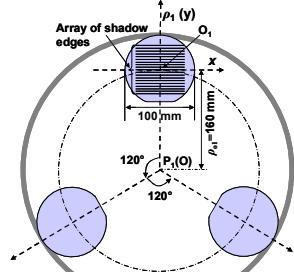
## Experimental—with Compensation During 1<sup>st</sup> Al Deposition



Side view

Assuming cosine distribution law for a planar area source:

$$h = A \cos \phi \cos \alpha / r^2$$



Top view

Al thickness distribution:

In  $\rho_1(y)$  direction for the central line ( $x=0$ ):

$$h(\rho_1) = h_0 / [1 + (\rho_1 / H_{vl})^2]$$

In  $x$  direction for the central line ( $y=0$ ):

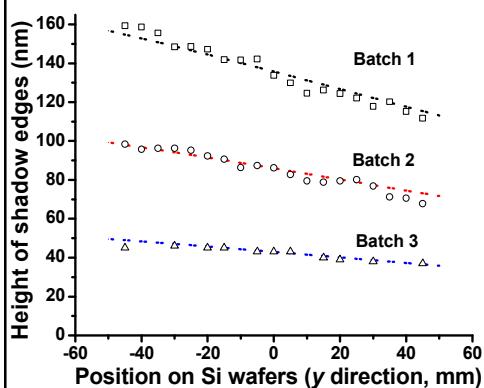
$$\frac{h(x)}{h(0)} = \frac{(H_{vl}^2 + \rho_{ol}^2)^2}{(H_{vl}^2 + \rho_{ol}^2 + x^2)^2} = \frac{(411^2 + 160^2)^2}{(411^2 + 160^2 + x^2)^2}$$



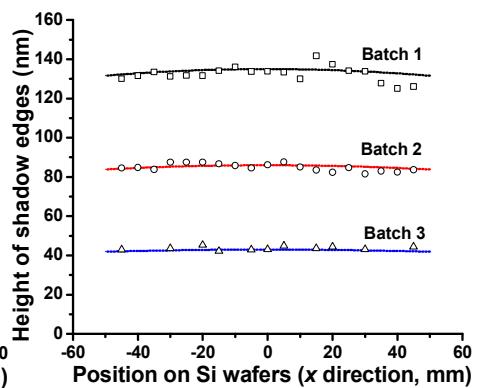
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## Experimental Results—with Compensation During 1<sup>st</sup> Al Deposition



Al thickness along y direction on Si wafer



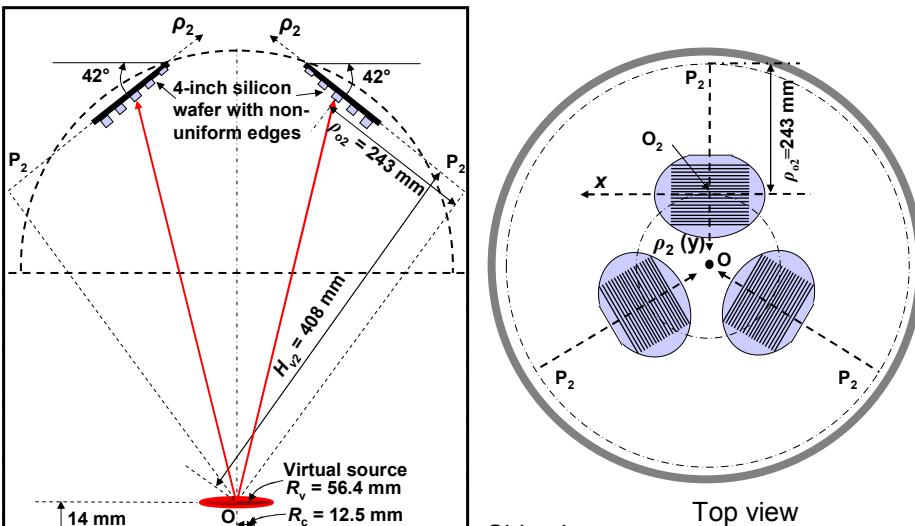
Al thickness along x direction on Si wafer



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## Experimental—with Compensation During 2<sup>nd</sup> Al Deposition

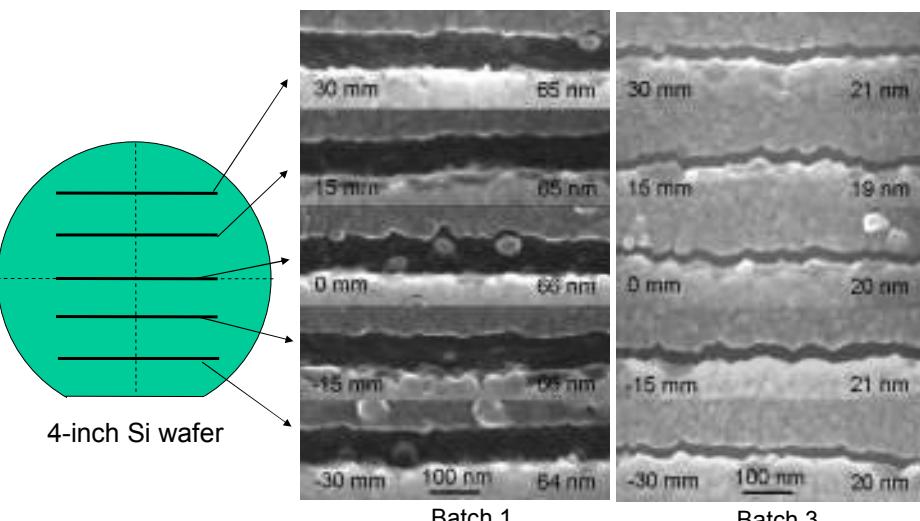


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## Experimental Results with Compensation

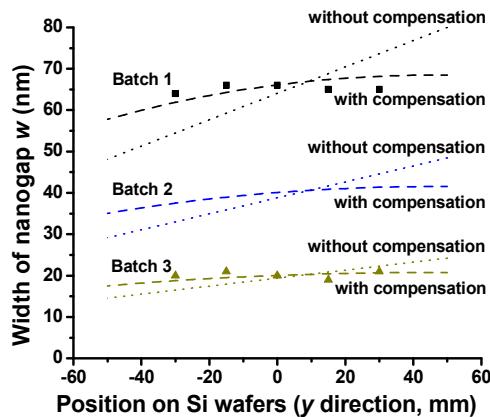


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## Experimental Results —with Compensation



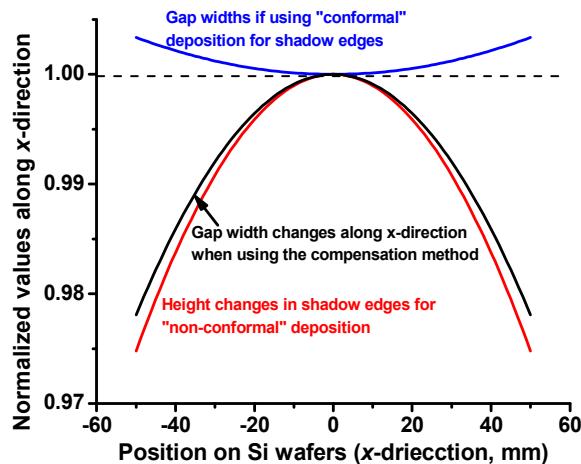
Nanogap width distribution along the  $y$ -direction of the 4-inch Si wafers  
 $\pm 1\text{nm}$  variation across 4in wafers.



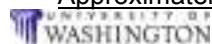
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## Nanogap Width along X Direction with Compensation



Normalized distribution along the  $x$ -direction of the 4-inch Si wafers  
Approximately 2% variation is expected, which is not measurable.



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# **Extension of Shadow Edge Lithography**



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**Experimented by**  
**John G. Bai, Ph.D.**  
**Kieseok Oh**  
**WoonHong Yeo**



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## **Reference**

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